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RECORDATION DATE: 05/03/2001

REEL/FRAME: 011776/0678

NUMBER OF PAGES: 3

BRIEF: ASSIGNMENT OF ASSIGNOR''S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

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DOC DATE: 05/02/2001

ASSIGNOR:

SMITH, GREGORY J.

DOC DATE: 05/02/2001

ASSIGNEE:

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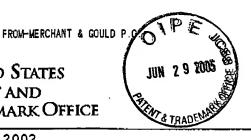
SERIAL NUMBER: 09848625

PATENT NUMBER:

FILING DATE: 05/03/2001

ISSUE DATE:

SHAREILL COLES, EXAMINER ASSIGNMENT DIVISION OFFICE OF PUBLIC RECORDS



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MARCH 19, 2002

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RECORDATION DATE: 11/08/2001

REEL/FRAME: 012492/0345

NUMBER OF PAGES: 3

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

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DOC DATE: 11/05/2001

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SERIAL NUMBER: 09848625

PATENT NUMBER:

FILING DATE: 05/03/2001

ISSUE DATE:

SAUNDRA BALLENGER, EXAMINER ASSIGNMENT DIVISION OFFICE OF PUBLIC RECORDS

FROM-MERCHANT & GOULD P.C.

JUL 2 0 2005

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. :

US 6,768,355 B1

PAGE 1 of 1

DATED

July 27, 2004

INVENTOR(S):

Paul M. Henry et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page:

Beginning "(73) Assignee:

National Semiconductor Corporation,

Santa Clara, CA (US)"

should read -- (73) Assignee: National Semiconductor Corporation,

Santa Clara, CA (US); and

Motorola, Inc.

Schaumburg, IL (US)--

Column 6, Lines 63 and 64: "not inadvertently rest" should read --not inadvertently reset--

MAILING ADDRESS OF SENDER:

Merchant & Gould P.C. Attn: Brett A. Hertzberg P.O. Box 2903 Minneapolis, MN 55402-0903" PATENT NO.

US 6,775,112 B1

Docket No.

50019.51US01/P04881

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